

## A10-3588 I2C Interface Describe

MCU I2C Address: 0x18, Document Version1.0 Create 241119 Kenny

ADDR	REG NAME	BYTE	R/W	STORE	DEF VALUE	REG NOTE
0x00	MAC1	6	R/W	ROM	-	MAC for NIC 1
0x06	MAC2	6	R/W	ROM	-	MAC for NIC 2
0x0C	MAC3	6	R/W	ROM	-	MAC for NIC 3
0x12	USID	9	R/W	ROM	-	17-character USID
0x1B	EN_TEST1	1	R/W	ROM	0	[Reserved] Factory Test Flag 1
0x1C	EN_TEST2	1	R/W	ROM	0	[Reserved] Factory Test Flag 2
0x1D	VERSION	2	R	RAM	-	MCU Firmware Version
0x20	BOOT_MODE	1	R/W	ROM	1	Boot Configuration: 0 - Boot From SPI FLASH First 1 - Boot From EMMC First
0x21	EN WOL	1	R/W	ROM	0	Mainboard(A10-3588) WOL (Wake-on-LAN) Configuration: 0: Disable WOL 1: Enable WOL to wake and boot 2: Parameter Unused 3: Enable WOL Reset
0x22	EN RTC	1	R/W	ROM	0	RTC Boot Configuration: 0 - Disabled, 1 - Enabled
0x23	EN DCIN	1	R/W	ROM	0	DCIN(Power Up) Boot Configuration: 0 - Disabled 1 - Enabled
0x26	EN_WOL_IOB	1	R/W	ROM	0	Baseboard(ACC-A9A10) WOL (Wake-On-LAN) Configuration: 0: Disable WOL 1: Enable Boot From WOL 2: Enable WOL But Baseboard is Not Connected (Read-Only) 3: Enable Boot and Rest From WOL
0x27	SPI_BT_SW	1	R/W	RAM	1	RK3588 SPI and UART GPIO Function Switching: 0 - UART for BT 1 - SPI
0x2A	LED_USER	1	W	RAM	-	User LED Configuration (External Connector) : 0: OFF 1: ON 2: BREATH 3: HEARTBEAT
0x2B	EN_SLEEP	1	R/W	ROM	0	MCU Sleep Configuration: 0 - Disabled 1 - Enabled Sleep, After System Shutdown
0x2C	EN_MAC1	1	R/W	ROM	0	MAC 1 Usage Flag(Only for Flag Store, no Function, This Function Depend on Host Define): 0: Not Used 1: Used
0x2D	EN_MAC2	1	R/W	ROM	0	MAC 2 Usage Flag(Only for Flag Store, no Function, This Function Depend on Host Define): 0: Not Used 1: Used
0x2E	EN_MAC3	1	R/W	ROM	0	MAC 3 Usage Flag(Only for Flag Store, no Function, This Function Depend on Host Define): 0: Not used 1: Used
0x2F	EN_DC_OUT	1	R/W	ROM	2	Mainboard(A10-3588) DC OUT to Baseboard(ACC-A9A10) Configuration : 0: Auto- Detection Mode: Will Enable Mainboard DC OUT While Baseboard no Power to Mainboard 1: Manually Enable DC OUT. 2: Manually Disable DC OUT.
0x80	RST_CONF	1	W	RAM	-	Configuration Reset: 0x00: Reset System Configuration (MAC and USID Will Not Reset). 0xF0: Reset the Entire Configuration.
0x81	RST_SYS	1	W	RAM	-	Reboot Command: 0: Reboot from SPI Flash. 2: Reboot from EMMC. 1: Reboot only.
0x82	FAN_LEVEL	1	W	RAM	-	Colling Fan Speed Configuration: 0: OFF 1: 20% 2: 30% 3: 40% 4: 50% 5: 60% 6: 70% 7: 80% 8: 90% 9: 100%
0x83	FAN_TEST	1	W	RAM	-	Colling Fan Test: 1: Enable test(Enable 100% Speed for 2 Seconds. )
0x84	EN_WDT	1	W	RAM	-	Watchdog Timer Configuration: 0: Disabled 1: Enabled Note: The Host Outputs Pulses at 500ms, And The MCU Detect at 1000ms.
0x85	INIT_WOL	1	W	RAM	-	Mainboard NIC Initialization Flag: 0: NIC Initialization Start. 1: NIC Initialization Completed.
0x86	SYS_OOPS	1	R	RAM	-	System Unexpected Shutdown Flag (Used When WOL is Enabled): 0: Shutdown Normally. 1: Shutdown Unexpected.
0x87	BOOT_FLAG	1	R	RAM	-	Boot Status Flag: 0: Normal Boot. 1: Boot From SPI With the Func Key Pressed at Startup. 2: Reboot From SPI. 3: Reboot From EMMC. 4: Reboot From EMMC With the PWR Key Pressed.at Startup
0x88	LINK_JN	1	R	RAM	-	Link Connection Flag: 0: Link Connect 1: Link Disconnect.
0x89	INIT_WOL_IOB	1	W	RAM	-	Baseboard NIC Initialization Flag: 0: NIC Initialization Start. 1: NIC Initialization Completed.
0x8A	LINK_5V_OUT_EN	1	R/W	RAM	-	Mainboard 5V Output to Baseboard Configuration: 0: OFF 1: ON
0x8B	FLASH_BUSY	1	R	RAM	-	MCU Flash Write Busy Flag: 0: Idle 1: Busy